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STATEMENT UNDER 37 CFR 3.73(b)				
Applicant/Patent Owner: _	Ellison, Darmawaskita e	et al.		
Application No./Patent No.:	: 09/769680	Filed/Issue Date:	1/25/2001	
Entitled: Functional Pathway Configuration at a System/IC Interface (DM-A556)				
Microchip Techn	ology Inc.		, a corporation	
(Name of Assignee) (Type of Assignee, e.g., corporation, partnership, university, gov			tion, partnership, university, government agency, etc.)	
states that it is:				
1. X the assignee of	the assignee of the entire right, title, and interest; or			
2. an assignee o	an assignee of less than the entire right, title, and interest in			
(The extent (b	(The extent (by percentage) of its ownership interest is %); or			
	The assignee of an undivided interest in the entirety of (a complete assignment from one of the joint inventors was made)			
the patent application/pater	nt identified above by vi	rtue of either:		
A. An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel 011486 /Frame 0919 , or for which a copy thereof is attached.				
OR				
B. A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as shown below:				
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assignee was, or concurrer [NOTE: A separate cop	ntly is being, submitted f by (i.e., a true copy of th ance with 37 CFR Part 3	for recordation pursuant to 3 ne original document(s)) mus	n of title from the original owner to the 7 CFR 3.11. It be submitted to Assignment recorded in the records of the USPTO.	
The undersigned (whose ti	tle is supplied below) is	authorized to act on behalf o	of the assignee.	
/Bruce W.	. Slayden II/		May 1, 2009	
Sign	nature		Date	
Bruce W. Slayden II		Atı	torney for the Applicant	
Printed or Typed Name			Title	

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